DATA SHEET

The Model 9002 Interface Controller allows the Decision Data Computer Corporation's Model 8010 Interpreting Data Record (Card Reader/Punch Verifier) equipment to be interfaced to the CPU of any Wang configuration which possesses the $GIO instruction set. (The following CPU's can interface with the Model 8010 80-column Card Reader/Punch: 2200C with Option 2, 2200S with Option 23 or 24, 2200T, WCS-20 and WCS-30.) The Interface Controller board plugs into any I/O slot on the CPU chassis and has two 50-pin female Amphenol connectors to facilitate data transfers between the System 2200 and the Model 8010 Card Reader/Punch equipment. Two 10-ft cables (32-pair twisted, 24 guage wire) with 50-pin male Amphenol connectors on both ends are also supplied with the controller board.

The Model 8010 Reader/Punch is used to keypunch, verify, gang punch, interfile, print, interpret and reproduce 80-column cards. The 2200 System controls the input and output operations of the Model 8010 Reader/Punch with the $GIO statement. With a technique similar to machine language programming, interface operations can be custom-tailored for special applications by using a sequence of microcommand codes to produce a desired signal sequence.

The Model 9002 interface design treats the Model 8010 unit as a two cycle device. The first cycle takes a card from the input hopper through the read station to the wait station; the second cycle takes the card from the wait station through the punch station and print station to the stacker. The following interface functions are available under System 2200 program control:

- Check the status of the reader/punch.

- Feed a card from the wait station.

- Clear a card from the wait station.
  (Optionally feed next card from hopper.)

- Read a card in Hollerith.
  (Optionally stack card.)
  (Optionally feed next card from hopper.)
- Read a card in binary.
  (Optionally stack card.)
  (Optionally feed next card from hopper.)

- Punch a card in Hollerith and stack; no printing.

- Punch a card in Hollerith and stack, print punched data.
  (Optionally feed next card from hopper.)

- Punch a card in Hollerith, print separately provided data and stack.
  (Optionally feed next card from hopper.)

- Print a card without punching and stack.
  (Optionally feed next card from hopper.)

- Punch a binary card without printing and stack.
  (Optionally feed next card from hopper.)

- Punch a binary card; print with separately provided data and stack.
  (Optionally feed next card from hopper.)

MODEL 9002 INTERFACE CONTROLLER

With the exception of loading programs with the LOAD statement, the above operations are performed using different I/O control microcommands of the $GIO statement.

SPECIFICATIONS

Size:

Length . . . . 14 in. (35.6 cm)

Width . . . . 7.5 in. (19.1 cm)

Depth . . . . 1.2 in (3.2 cm)

Weight:

2 lb (0.9 kg)

Power Requirements:

Supplied by CPU

CPU Compatibility

2200C with Option 2, 2200S with Option 23 or 24, 2200T, WCS-20, or WCS-30.

Connector:

Two 50–pin female Amphenal connectors are mounted on the controller.

Cabling:

Two 10 ft (3 m) cables with matching male connectors on both ends are supplied with the unit.

Operating Environment:

50° F to 90° F (10° C to 32° C)

20% to 80% relative humidity

NOTE:

Wang Laboratories does not assume any responsibility for the connection or maintenance of the Decision Data 8010 card reader/punch equipment.

ORDERING SPECIFICATIONS

An interface controller which provides compatibility between System 2200 configurations with a $GIO instruction set and card reading capability and the Decision Data Computer Corporations Model 8010 card reader/punch equipment. The interface controller board must plug into any I/O slot of the CPU and must have two 50-pin female Amphenol connectors to facilitate data transfers between the System 2200 and the Model 8010.

Wang Laboratories reserves the right to change specifications without prior notice.